

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)		Atty Docket No. ALTRP085/ A880 Applicant: Examiner Helen Rossoshek Eric Teh Gim Aik Filing Date Herewith	Application No.:10/601,015 Please assign Group AU 2825 Please assign
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	C							
	D							
	E							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication		
	F	http://biz.yahoo.com/bw/020130/300146_1.html Page no longer available		HR
HR	G	Using Electric 9-2: Design-Rule Checking, http://www.staticfreesoft.com/manual/text/chap09-02.html	06-2003	
	H	Robert N. Mayo, Magic Tutorial #6: Design-Rule Checking, http://infopad.eecs.berkeley.edu/~icdesign/magic/tut6.html	06-2003	
	I	Celebrity IC Design and Verification Software, SILVACO International. http://www.silvaco.com/products/legacy/celebrity/celebrity/celebrity.pdf	06-2003	
	J	CyberCut A Network Manufacturing Service, Design Rule Checking. http://cybercut.berkeley.edu/html/research/project_rule_checker.htm	06-2003	
↓	K	Randall et al., Design Rule Checking and Pattern Processing for Advanced MEMS Designs http://www1.zyvex.com/TEXMEMS3/Abstracts/JRandall.html	06-2003	
HR	L	P3 Design Rule Checker, Hierarchical and Incremental Design Rule Checker, http://www.phase3.com/p3v-dsnchk.htm	06-2003	
Examiner	/Helen Rossoshek/		Date Considered	08/09/2006

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.